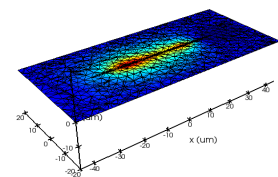
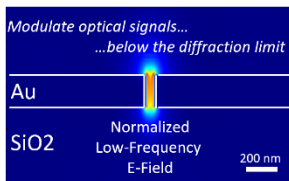


# Master Thesis / Semester Project / Internship

## Photonic Integrated Circuits: Device Design & Simulation (f/m/d)

Broaden your integrated optics horizon at Polariton Technologies



Polariton Technologies is a young start-up coming out of ETH Zurich developing cutting-edge devices for optical communications. We are about to launch our first product based on a novel electro-optic modulator. It enables the customer to send data with unprecedented speed and quality while cutting energy cost by >50%. Our ultra-compact modulators are used in academia and industry for optical communication links. We are looking for you to join our team and to develop the next generations of our products with us.

### Your job includes:

- Design, simulate, and optimize photonic integrated circuit devices using state of the art EM simulation & design software while working in a dynamic team
- Perform optical, electrical, electro-optical or multi-physics simulations
- Evaluate and analyze product relevant performance figures

### As an ideal candidate, you have

- A bachelor degree in an engineering science, physics, or similar
- Good understanding of physics, optics and/or electronics
- First experience with electric-field and/or multi-physics simulation software is a plus
- Highly motivated to develop, optimize and understand integrated optical devices working with state-of-the-art simulation software

### Your learnings

- Grow with a dedicated and dynamic team in a start-up work environment
- Contribute to innovative technology development and to new products
- Work on cutting-edge integrated optics, strengthen your simulation skills & get hands on experience on how to build electronic-photonic circuits from scratch

### Your application

If you are interested in working with us, please send your application (motivation letter, CV) to Wolfgang Heni: [join@polariton.ch](mailto:join@polariton.ch)

Starting time: after negotiation

Location: Säumerstrasse 4, 8803 Rüschiikon, remote working possible

